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TO: Commissioner for Patents
Attn: Wai-Sing Louie
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Washington, D.C. 20231FROM: Edward J. Brooks, IIIOUR REF: 303.382US2TELEPHONE: (612) 373-6913FAX NUMBER (703) 872-9319*** Please deliver to Examiner Wai-Sing Louie in Art Unit 2814. ***Document(s) Transmitted: A Terminal Disclaimer (2 pgs.) and a Petition for Extension of Time (1 pg.)Total pages of this transmission, including cover letter: 4 pgs

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In re. Patent Application of: Joseph E. Geusic et al.Examiner: Wai-Sing LouieSerial No.: 09/618,648Group Art Unit: 2814Filed: July 18, 2000Docket No.: 303.382US2Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE INTERCONNECTS FORMED THROUGH A SEMICONDUCTOR WAFER AND METHODS FOR FORMING SAME

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